

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,965,264 B1
DATED : November 15, 2005
INVENTOR(S) : Wai Cheong Chan et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [54], Title, delete "**ADAPTIVE THRESHOLD SCALING CIRCUIT**" and substitute -- **ADAPTIVE THRESHOLD SCALING** --.

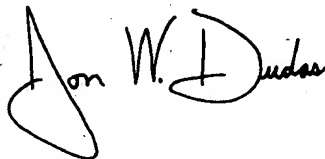
Column 6.

Line 7, after "transistors" insert -- , --.

Line 23, delete "claim 11", and insert -- claim 10, --.

Signed and Sealed this

Twenty-fifth Day of April, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office